

MAT-8716US

PATENT

RECEIVED 27 JUN 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: To Be Assigned
Applicant: Toshiaki Takenaka, et al.
Filed: Herewith
Title: METHOD AND APPARATUS FOR MANUFACTURING CIRCUIT BOARD
TC/A.U.: To Be Assigned
Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (RP) submitted herewith. A copy of the references listed on the 1449 form is enclosed.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

The following Japanese references are not in the English language:

- 1) 2001-213064
- 2) 57-103862
- 3) 2001-7514
- 4) 2002-171060
- 5) 6-268345
- 6) 7-106760

10/540606

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References 1), 3) - 6) include an English language abstract. Reference 2 includes a partial English translation. References 5) and 6) include a "concise explanation" under 37 CFR § 1.98(a)(3) as set forth in the specification at page 1, line 15.

This Information Disclosure Statement is being filed concurrently with the above-referenced application.

Respectfully submitted,

Lawrence E. Ashery, Reg. No. 34,515
Attorney for Applicants

LEA/dmw

Enclosures: PTO Form 1449
(6) References

Dated: June 27, 2005

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The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. **18-0350** of any fees associated with this communication.

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I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Kathleen Libby

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(AW 08/03)

Substitute for Form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

SHEET 1 of 2

Complete if Known

Application Number	To Be Assigned 10/540606
Filing Date	Herewith
First Named Inventor	Toshiaki Takenaka, et al.
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket No.	MAT-8716US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		2001-213064 A (with English abstract)	08/07/2001	MATSUSHITA ELECTRID IND CO LTD		<input type="checkbox"/>
		57-103862 U (with partial English translation)	06/26/1982	HITACHI LTD		<input type="checkbox"/>
		2001-7514 A (with English abstract)	01/12/2001	NGK SPARK PLUG CO LTD		<input type="checkbox"/>
		2002-171060 A (with English abstract)	06/14/2002	MATSUSHITA ELECTRIC IND CO LTD		<input type="checkbox"/>
		6-268345 A (with English abstract)	09/22/1994	MATSUSHITA ELECTRIC IND CO LTD		<input type="checkbox"/>
		7-106760 (with English abstract)	04/21/1995	MATSUSHITA ELECTRIC IND CO LTD		<input type="checkbox"/>

Examiner
Signature

/Brian Talbot/

Date
Considered

11/13/2008

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

⁶Applicant is to place a check mark here if English language translation is attached.

The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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